



User Manual

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With kind regards,

Your opinion:

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L1 and L2 POS Motherboard

User Manual

Edition November 2018

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Overview

Introduction

This manual describes the features of two variants of a Motherboard based on the Intel 8 series chipset Q87 and H81, formerly known as Lynx Point.

These **L1.x** and **L2.x** Motherboards were primarily designed for the Wincor Nixdorf Systems EPC_5G, EPC_5G_DC and EPC_5G_Upgrade.

Some Highlights of the L1 and L2 Motherboard

- 4th Generation Intel[®] Core[™] Processors, formerly known as Haswell
- Intel 8 series chipset Q87 and H81, formerly known as Lynx Point
- CPU integrated graphic controller up to Intel[®] HD Graphics 4600, depending on used processor
- AMT 9.1 support at L1 motherboards
- 1x VGA Interface
- 1x DVI-D interface
- Gigabit LAN onboard (Intel[®] Ethernet Connection; Q87: I217-LM, H81: I217-V)
- 3 SATA ports (Q87: 3xSATA III, H81: 2x SATA III + 1x SATA II)
- 2x DDR3 SODIMM sockets, supporting up to 16GB (2x8GB) at 1600MHz. Only ~3GB available for 32bit OS
- 1x PCI Express x16 gen3
- 2x PCI Express x1 gen2
- 2 Standard COM ports
- 1 LPT port
- MiniPCIe socket (full size) supporting the WN NVRAM module
- 2 USB3.0 ports
- Q87: 12 USB2.0 ports, H81: 8x USB ports

- TPM onboard
- PC Beeper onboard



TFT- displays without DDC are not supported.

Motherboard Specification



Internal Connectors



Mainboard assembly variants

As mentioned above there are two motherboard variants: The **L1** board with Q87 chipset supporting AMT and RAID and the **L2** board with H81 chipset as value edition supporting lesser features.

CPU	CPU#	#CPU	#Threads	GHz	GHz GFX	Cache	TDP
		core		Core		size	[W]
i5	i5-4570TE	2	4	2.7 (3.3)	0.35 (1)	4MB	35
i5	i5-4570S	4	4	2.9 (3.6)	0.35(1.15)	6MB	65
i3	i3-4330TE	2	4	2.4	0.35 (1)	4MB	35
i3	i3-4330	2	4	3.5	0.35 (1.15)	4MB	54
Pentium	G3320TE	2	2	2.3	0.35 (1)	3MB	35
Pentium	G3420	2	2	3.2	0.35 (1.15)	3MB	53
Celeron	G1820TE	2	2	2.2	0.35 (1)	2MB	35
Celeron	G1820	2	2	2.7	0.35 (1.05)	2MB	53

CPU support

Mainboard internal connectors and onboard features

USB 2.0 (internal)

6 USB ports at L1 board and 2 USB ports at L2 are routed to 10pin double row headers with 2.54 mm pitch. Each header provides 2 ports. These headers are intended to connect optional front USB modules or USB hubs.

+5V 1		2 +5V
P0-3	66	4 P1-
P0+ 5	66	6 P1+
GND 7		8 GND
Key 9		10 GND

Two USB ports share one fuse. USB port 11 at header USB3 (pins 1,3,5,7) is shared with PCIe x1 connector PCIE3 (pins A5 to A8) for USB uplink connection for the "Retail Card".

Mini PCI Express

The mainboard provides a full size mini PCI Express (rev.1.1) / mSATA connector. It is placed on top, thus being accessable without removing other components. It supports LPC signals including serial interrupts. Auto detection by mSATA presence detect supports automated switch between mSATA and PCIe functionality (PCIe functionality only supported in conjunction with Q87 chipset according to mini PCI Express Standard Rev 1.1). The **L1 motherboard** supports PCIe **and** mSATA functionality, the **L2 motherboard** supports mSATA functionality only. **Connector type**: Standard MiniPCIe 1.1 connector full size

Pin	Function	Pin	Function
1	WAKE	2	VCC3
3	n.c.	4	GND
5	n.c.	6	VCC 1.5V
7	CLKRQ#	8	FRAME#
9	GND	10	LAD3
11	REFCLK-	12	LAD2
13	REFCLK+	14	LAD1
15	GND	16	LAD0
17	SUSCLK 32KHz	18	GND
19	CLK 33MHz	20	n.c.
21	GND	22	RESET#
23	PCIE_RX- / SATA +B	24	3VSB
25	PCIE_RX+ / SATA -B	26	GND
27	GND	28	VCC 1.5V
29	GND	30	SMB_CLK
31	PCIE_TX- / SATA -A	32	SMB_DATA
33	PCIE_TX+ / SATA +A	34	GND

35	GND	36	n.c. (USB-)
37	GND	38	n.c. (USB+)
39	VCC3	40	GND
41	VCC3	42	n.c.
43	GND	44	n.c.
45	n.c.	46	n.c.
47	n.c.	48	VCC 1.5V
49	n.c.	50	GND
51	mSATA presence detect	52	VCC3

Memory (internal)

The mainboard provides two DDR3 SODIMM sockets supporting up to 16GB in dual channel mode. The horizontal mounting of the SODIMM sockets ensures an optimal air flow.

SATA (internal)

The mainboard provides three standard SATA ports. The ports SATA1 (white) and SATA2 (blue) supporting SATA III connectivity speed. Port SATA3 (black) supports SATA III speed at L1 board, and SATA II speed at L2 board. The RAID functionality is only supported by the L1 motherboard.

Fan (internal)

The mainboard provides two fan connectors. The CPU fan connector supports PWM fans with 4 pin connection.

Connector details are: 2.54mm (.100") Pitch Vertical Header, with Friction Lock, 4 (3) Circuits, PC Tail Length: 3.50mm (.138"). Molex Part Nr: 47053-1000 or similar. The connector follows the Intel "4-Wire Pulse Width Modulation (PWM) Controlled Fans" specification.

PWM Fan

Pin	Signal	Signal Description	
1	GND	GND	
2	12 V	Fan operation voltage	
3	Sense	Tachometric signal	
4	Control	PWM control signal (only	
		4 pin connector)	



The PSU fan connector has 3 pin connection with DC fan speed regulation.

			_DC
Pin	Signal	Signal Description	Fan
1	GND	GND	
2	PWR	Operation Voltage 6-12 V	
3	Sense	Tachometric signal	



Chassis intrusion connector (internal)

The mainboard supports a chassis intrusion connector. Type: 3 pin shrouded header, B3B-PH-K-S (JST) or equivalent.

Pin Number	Function
1	GND
2	Intrusion input (switch to GND if chassis is open)
3	n.c.



Front panel connector (internal)

The mainboard supports a front panel connector to support service elements (like POWER ON pushbutton, HDD and power LEDs). The Power LED at the chassis front connected to this front panel header is green when system is powered on (S0). During Stand By (S3) it blinks green and when system is in hibernation (S4) or powered down (S5) the LED is orange.

Power Status	Power LED
S0	Green
S3	Green blinking
S4, S5	Orange

The BIOS is able to disable the power button. This feature is automatically enabled when "Follow AC power" is selected within the BIOS setup. Then it is not possible to switch off the system by the power button, even when pressed longer than 4s. But when the OS is shut down correctly, system can be switched on again by the power button.

The front panel header also supports speaker connection.

Pin Number	Function
1	Power switch +
2	Reset switch +
3	Power switch -
4	Reset switch -
5	Power LED +
6	Speaker -
7	Power LED -
8	Coding
9	HDD LED +
10	GND
11	HDD LED -
12	Speaker +

Type: 2x6 pin header, 2.54 mm pitch.

12		11
10 🗖	D	9
8	D	7
6	D	5
4	D	3
2		1

ATX / 12V Power connector (internal)

Pin Number	Function
1,2,12,13	+3.3V
3,5,7,15,17,18,19,24	GND
4,6,21,22,23	+5V
8	Power ok
9	5V SB
10, 11	+12V
14	-12V
16	PSON
20	n.c.

The mainboard provides a 4 pin and a 24 pin ATX power connector.

(+3.3V) 1	00	13 (+3.3V)
(+3.3V) 2	00	14 (-12V)
(Ground) 3	00	15 (Ground)
(+5V) 4	• •	16 (PS-ON)
(Ground) 5	•••	17 (Ground)
(+5V) 6	•	18 (Ground)
(Ground) 7	00	19 (Ground)
(PG) 8	$\mathbf{\circ}\mathbf{\circ}$	20 (-5V)
(+5VSB) 9	• •	21 (+5V)
(+12V) 10	<u>o</u>	22 (+5V)
(+12V) 11	0	23 (+5V)
(+3.3V) 12	•••	24 (Ground)



Pin Number	Function
1,2	GND
3,4	+12V

A 20 pin ATX power cable can be plugged into the 24 pin connector on the motherboard, too. In this case the pins 11,12,23 and 24 are not used and left open. The 4 pin ATX power connector has to be connected to the PSU anyway, otherwise the motherboard will not work.

LPT (internal)

Connector type: 26 pin shrouded header, 2.54mm pitch

Pin	Function	Pin	Function	
1	STR#	14	GND	
2	AFD#	15	D6	
3	D0	16	GND	
4	ERR#	17	D7	
5	D1	18	GND	
6	PINIT#	19	ACK#	
7	D2	D2 20 GND		
8	LPT_SLIN#	21	BUSY	
9	D3	22	GND	
10	GND	23	PE	
11	D4	24 GND		
12	GND	25 SLCT		
13	D5	26 Key		



TPM (onboard)

The mainboard provides a Trusted Platform Module (TPM) IC from Infineon. The Lx.0 boards provide the SLB 9635TT1.2 with firmware 3.19, the Lx.1 boards the SLB 9660TT1.2 with firmware 4.40. The SLB 9660 is able to be upgraded to TT2.0 by a firmware update. During this update all data, provisioning, certifications and keys stored within the TPM IC will be lost. For identification of the firmware there is an additional identifier in the POST while the TPM is active:

TT1.2 on SLB 9635 has identifier #TPM

TT1.2 on SLB 9660 has identifier #TPM1

TT2.0 on SLB 9660 has identifier #TPM2

The TPM IC is directly soldered on the PCB.



Jumper 4 (JP4):

Enables/Disables the TPM Chip. Default setting is position 1-2, TPM enabled. To disable the TPM set Jumper to position 2-3.

Jumper 3 (JP3):

Sets the physical presents (PP). Default setting is position 2-3, PP disabled. To enable PP set Jumper to position 1-2. TT2.0 is not supporting the PP feature any more.

PCI Express Slots (internal)

There are two PCIe slots x1 and one 1 PCIe slot x16.

Use in the systems BEETLE /MIII, EPC_5G, EPC_5G_DC or EPC_5G_Upgrade

Only low profile cards are supported.

Height 68.90 mm (2.731 inches) maximum, Length 167.65 mm (6.600 inches) maximum

PCIe cards are able to wake up the system.

Thermal management: The input power of a single PCIe x1 card must not exceed 10W and x16 card does not exceed 30W.

The PCIe slot at the outer side of the mainboard supports USB signals on PCIe pins A5 (D-) and A8 (D+) as USB uplink connection and A6+A7 (VBUS) to Retail specific PCIe card.

The same USB port is routed to the USB3 connector.

Onboard power button and status LED (internal)

The motherboards support one onboard power button in rear I/O shield area. The button's position protects it from being touched accidentally or being pushed by cables.

The motherboards support 2 dual color onboard LEDs indicating the power status (green) and HDD activity (red). The LEDs are visible outside of the chassis through a chassis hole.

Power status	Power LED
S0	Green
S3, S4, S5	Off

Onboard status LEDs and power button work together with an external status LED and power button connected to the front panel connector.

Mainboard onboard connectors with external access

Motherboard I/O shield overview (external)

The picture below shows the arrangement of the onboard I/O connectors.



DVI-D (external)

This intervace uses a standard DVI-D connector supporting a single link connection to a digital display. Only displays with DDC are supported.

VGA (external)

This interface uses a 15 pin DSUB connector in the upper row of I/O shield. Sync signals VSYNC and HSYNC have 5V logic high level.

LAN (external)

The mainboard supports 1Gbit connection to a Local Area Network (LAN). Indication **LED** for link and activity is available.

Right LED	Speed Indication
10mbit	off
100mbit	green
1000mbit	orange

Left LED	Link&Activity
Link at every speed	Yellow on
Traffic at every speed	Yellow blinking

WOL (wake on LAN) and PXE are supported.

USB 2.0 / USB 3.0 (external)

8 USB ports (6 for USB2.0 and 2 for USB3) are located in the mainboard I/O connector area.

COM (external)

There are two standard RS232 COM ports (male 9pin DSUB connectors) at the I/O shield.

Audio (external)

The mainboard supports a microphone-in, a line-out and a line-in connector in I/O shield. The connector type is stereo 3.5mm diameter.

Technical Data

Торіс	Remarks	
Form Factor	μΑΤΧ 226x210mm	
Processor	4th Generation Intel [®] Core [™] Processors	
Chipset	Intel Series 8 (L1 : Q87; L2 : H81)	
CPU Socket	LGA1150	
Graphics	Celeron 1820: Intel HD Graphics Core i3: Intel HD 4600 Core i5: Intel HD 4600	
Main Memory	Dual channel memory architecture 2 sockets supporting unbuffered non-ECC DDR3 memory modules supporting up to 16GB system memory SODIMM	
Storage	3x SATA III (L1) 2x SATA III + 1x SATA II (L2) RAID support (L1 only)	
LAN	onboard Gigabit LAN, status LED activity and link	
Audio	Realtek HD audio Codec ALC 662	
USB	2x USB 3.0 12x USB 2.0 (L1) 8x USB 2.0 (L2)	
Expansion Slots	2x PCIe x1 1x PCIe x16 slot 1x MiniPCIe 1.1 (used by L1 only), additional LPC signals	
Motherboard	1x DVI-D	Audio:
External I/O	1x LAN (RJ45) + 2x USB3.0	Line in (blue)
Connectors (at	6x USB2.0	Line out (green)
I/O Shield)	1x VGA	Microphone in (pink)
	1x COM	
	1x Power Button and Status LED	

Internal I/O Connectors	3x USB 2.0 headers supporting 2 ports each (L1) 1x USB 2.0 headers supporting 2 ports each (L2) 1x PCle x16 2x PCle x1 1x MiniPCle, v1.1 2x DDR3 SODIMM 3x SATA 1x CPU fan 4pin 1x PSU fan 3pin 1x LPT header
	1x Chassis intrusion 1x 24pin ATX 1x 4pin ATX
Other Special Features	1x MOS clear jumper TPM onboard Buzzer (PC Beeper) onboard
Product Lifecycle	5 years without changing OS software driver critical components e.g. audio codec, LAN, Super-I/O, Chipset
AMT	Version 9.1 at L1 board only



Hardware Monitor

The requirements for the hardware monitoring are described in the BIOS chapter Hardware Monitor.

Supported Sleep States

SO	Normal Operation ("ON")
S3	Suspend to RAM / "Stand By"
S4	Suspend to Disk / "Hibernation"
S5	Soft Off

RAID (L1 Motherboard)

The L1 motherboard provides RAID functionality for the SATA interface. Raid level 0 (striping), 1 (mirroring) and 5 (striping with distributed parity) are supported. For RAID 5 three HDD/SSD devices are needed. To enable the RAID functionality please see the corresponding chapter in the BIOS setup description.

For SATA ports 1 and 2 there are onboard LEDs indicating the status of the attached HDD/SSD within the RAID Array. The functionality of the SATA LEDs depends on host software (Intel SATA/RAID driver ("Rapid Storage Technology" – driver at the time of writing)). The host software needs to be installed and running for LED indication to work.



LED	Color	Function
D3	Yellow	Read/Write activity indication for SATA2 port
D4	Yellow	Read/Write activity indication for SATA1 port
D5	Red	Failure indication for SATA2 port
D6	Red	Failure indication for SATA1 port

Changing the Battery

The systems are equipped with a lithium battery on the motherboard to ensure data retention, the time and the setup parameters. The battery should be changed approximately every five years.



When inserting the new battery, make sure the polarity is correct. This is marked in the socket. Incorrect replacement of the battery may lead to the danger of explosion.

The battery is located in a socket on the Motherboard.



The lithium battery must be replaced only by identical batteries or types recommended by Diebold Nixdorf Systems.

You can return the used batteries to your Diebold Nixdorf Systems sales outlet. Batteries containing harmful substances are marked accordingly. The chemical denotations are as follows: **CD** = Cadmium; **Pb** = Lead, **Li** = Lithium.



This symbol on a battery tells you that batteries containing harmful substances must not be disposed of as household waste. Follow the country specific laws and regulations. Within the European Union you are legally bound to return these batteries to the service organization where you purchased the new battery.



The setup parameters must be reset each time the battery has been changed.

UEFI BIOS Setup

The mainboard L1.0-Q87-uATX-STD and L2.0-H81-uATX-STD comes with an AMI UEFI BIOS chip that contains the ROM Setup information of your system. This chip serves as an interface between the processor and the rest of the mainboard's components. This section explains the information contained in the Setup program and tells you how to modify the settings according to your system configuration.

Even if you are not prompted to use the Setup program, you might want to change the configuration of your system in the future. For example, you may want to enable the Security Password Feature or make changes to the power management settings. It will then be necessary to reconfigure your system using the BIOS Setup program so that the system can recognize these changes and record them in the NVRAM. All setup data is stored in a non-volatile memory (NVRAM). When you remove the battery, all parameters will be lost.

Standard UEFI BIOS Version

The UEFI BIOS ROM of the system holds the Setup utility. When you turn on the system, it will provide you with the opportunity to run this program.

This appears during the Power-On Self-Test (POST). Press <F2> to call the Set-up utility. If you are a missed the opportunity to pressing the mentioned key, POST will continue with its test routines, thus preventing you from calling Setup. If you still need to call Setup, reset the system by pressing <Ctrl> + <Alt> + . You can also restart by turning the system off and then on again. But do so only if the first method fails.

If you like to change the boot order only once, you can press the <F10> key during the POST is running. At the end you will see a Pop-Up window with all the devices the system has found. With the keys <UP> and <DOWN> you select the boot device.

The Setup program has been designed to make it as easy as possible. It is a menu-driven program, which means you can scroll through the various sub-menus and make your selections among the predetermined choices. When you invoke Setup, the main program screen will appear. Read more about the Setup entries on the following pages.

Because the UEFI BIOS software is constantly being updated, the following UEFI BIOS screens and descriptions are for reference purposes only and may not reflect your UEFI BIOS screens exactly.

BIOS Menu Bar

The top of the screen has a menu bar with the following sections:

Info	Use this menu for information only	
Main	Use this menu to make changes to the basic system configuration.	
Advanced	Use this menu to enable and make changes to the advanced features.	
Chipset	Use this menu to configure the chipset specific options	
Boot	Use this menu to configure the default system device used to locate and load the Operating System.	
Security	Use this menu to enable a supervisor or user password and Intrusion Detection.	
Save & Exit	Use this menu to exit the current menu or specify how to exit the Setup program.	



To access the menu bar items, press the right or left arrow key on the keyboard until the desired item is highlighted.

Legend Screen

The right frame displays the key legend. The keys in the legend frame allow you to navigate through the various setup menus. The following table lists the keys found in the legend with their corresponding alternates and functions.

Navigation Key(s)	Description of Functions	
← or → (keypad arrows)	Select the menu item to the left or right.	
↑ or ↓ (keypad arrows)	Moves the highlight up or down between fields.	
Enter	Move into sub menu or change selected menu items	
+ (plus key) - (minus key)	Change field contents.	
<tab></tab>	Jumps from one field to the next.	
<f1></f1>	Opens a general Help Screen with extended information.	
<f2></f2>	Load previous values (Load last saved values)	
<f3></f3>	Load optimized values (Factory reset)	
<f4></f4>	Saves changes and exits Setup.	
<esc></esc>	Opens a windows to select between exit and return to setup	

General Help

In addition to the Item Specific Help window, the UEFI BIOS setup program also provides a General Help screen. This screen can be called from any menu by simply pressing <F1>. The General Help screen lists the legend keys with their corresponding alternates and functions.

Scroll Bar

When a scroll bar appears to the right of a help window, it indicates that there is more information to be displayed that will not fit in the window. Use <PgUp> and <PgDn> or the up and down keys to scroll through the entire help document.

Press <Home> to display the first page, press <End> to reach the last page. To exit the help window, press the <Enter> or <Esc> key.

Sub-Menu

Note that a right pointer symbol "≻" appears left of certain fields. This pointer indicates that a sub-menu can be launched from this field. A sub-menu contains additional options for a field parameter.

To call a sub-menu, simply move the highlight to the field and press <Enter>. The sub-menu then will appear immediately. Use the legend keys to enter values and move from field to field within a sub-menu just as you would do within a menu. Use the <Esc> key to return to the main menu.

Take some time to familiarize yourself with each of the legend keys and their corresponding functions. Practice navigating through the various menus and sub-menus. If you accidentally make unwanted changes to any of the fields, use the set default hot key <F3>. While moving around through the Setup program, note that explanations appear in the Item Specific Help window located to the right side of each menu. This window displays the help text for the currently highlighted field.

Info Screen

When the Setup program is accessed, the following info screen appears:

This screen is for information only. There is nothing that could be changed within Setup. All information is intended to facilitate the support of your system.

Product Name:

This text is fixed for your mainboard with standard UEFI BIOS. This board is also called "L1.0-Q87-uATX-STD" or "L2.0- H81-uATX-STD"

BIOS Version:

The UEFI BIOS version is displayed in the WN release format xx/yy The UEFI BIOS Date is displayed the date of release in international format: MM/DD/YYYY

Ethernet MAC Address:

The Ethernet MAC-Address of the on board LAN Controller is displayed at this line if this device is enabled.

UUID Info:

A UUID is an identifier standard used in software construction, standardized by the Open Software Foundation. The intent of UUIDs is to enable distributed systems to uniquely identify information without significant central coordination.

System, Main board, Power Supply:

The default placeholders may be replaced by specific data from factory, describing configuration, serial number etc. for each device.

Main Menu

BIOS Information	
Bios Vendor	American Megatrends
Compliancy	UEFI 2.3.1; PI 1.2
Total Memory	2048 MB (DDR3)
Memory Frequency	1333 MHz
System Date	[Mo 07/07/2014]
System Time	[23:23:23]
Processor and Chipset Information	

System Date [MM/DD/YYYY]

Set your system to the date that you specify (usually the current date). The format is month, day, year. Valid values for month, day and year are: Month: (1 to 12), Day (1 to 31), Year: (up to 2100*).

*due to restrictions in leap year calculation within RTC implementation

System Time [XX: XX: XX]

Set your system to the time that you specify (usually the current time). The format is hour, minute, second. Valid values for hour, minute, and second are: Hour: (00 to 23), Minute: (00 to 59), Second: (00 to 59). Press <Enter> to terminate every entry value and reach the next position. On the upper right frame find the keys listed to modify the values.

Processor and Chipset Information

This enters a Sub-Menu with summaries of used processor and chipset.

Advanced Menu

ACPI Settings
Trusted Computing
Hardware Configuration
CPU Configuration
SATA Configuration
PCH-FW Configuration
AMT Configuration
USB Configuration
NCT6106D Super IO Configuration
NCT6106D HW Monitor
Intel TXT Information
Serial Port Console Redirection
AMI Graphic Output Protocol Policy
Network Stack Configuration
CMOS
Intel(R) Ethernet Network Connection
Driver Health

Sub Menu > ACPI Settings

ACPI Settings

[Enabled]

Enable ACPI Auto Configuration

Enable ACPI Auto Configuration

Being an ACPI BIOS system, the operating system is allowed to control the Power Management features of the computer and the setting for Advanced Power Management (APM) BIOS mode are ignored. Not all operating systems support ACPI BIOS mode.

Sub Menu ➤ Trusted Computing

<u>TPM1.2</u>

Configuration Security Device Support TPM State	[Enable] [Disabled]
Current Status Information TPM Enabled Status: TPM Active Status: TPM Owner Status:	Disabled Deactivated Unowned

Security Device Support

[Disable]

This option enables or disables the Trusted Platform Module. After change this option the system will reboot automated. If the security device is disabled the OS will not show this device in device list.

TPM State

[Disabled]

This option changes the actual state of the security module. Select the TPM operation after the next automated reboot of the system.

TPM2.0

Configuration				
Security Device Support	[Enable]			
Active PCR banks	SHA-1,SHA256			
Available PCR banks	SHA-1,SHA256			
SHA-1 PCR Bank	[Enabled]			
SHA256 PCR Bank	[Enabled]			
Platform Hierarchy	[Enabled]			
Storage Hierarchy	[Enabled]			
Endorsement Hierarchy	[Enabled]			
TPM2.0 UEFI Spec Version	[TCG_2]			
Physical Presence Spec Version	[1.3]			
TPM 20 InterfaceType	[TIS]			

Security Device Support

This option enables or disables the Trusted Platform Module. After change this option the system will reboot automated.

If the security device is disabled the OS will not show this device in device list.

SHA-1 PCR Bank	[Enabled]
Enable or Disable SHA-1 PCR Bank	
SHA256 PCR Bank	[Enabled]
Enable or Disable SHA256 PCR Bank.	
Platform Hierarchy	[Enabled]
Enable or Disable Platform Hierarchy.	
Storage Hierarchy	[Enabled]
Enable or Disable Storage Hierarchy.	
Endorsement Hierarchy	[Enabled]
Frankla an Disable Frankransant Lienansky	

Enable or Disable Endorsement Hierarchy.

TPM2.0 UEFI Spec Version

Select the TCG2 Spec Version Support, TCG 1 2: the Compatible mode for Win8/Win10, TCG_2: Support new TCG2 protocol and event format for Win10 or later.

Physical Presence Spec Version

Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.

[Enabled]

[TCG 2]

[1.3]
PCH LAN Controller	[Enabled]
Launch PXE OpROM policy	[Do not launch]
Wake On LAN	[Disabled]
Wake On Ring	[Disabled]
Wake On Time	[Disabled]
Wake On USB/PS2	[Enabled]
Wake on LAN (S3)	[Disabled]
Wake On PCIe Slot	[Disabled]
Restore AC Power Loss	[Follow AC Power]
Power Button	[Enabled]
BootOrder Menu	[Enabled]
VT-d	[Enabled]

PCH LAN Controller

[Enabled]

This option allows enabling or disabling the on-board LAN controller. Configuration options: [Disabled] [Enabled].

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Launch PXE OpROM policy

Do not launch]

Preboot Execution Environment (PXE) refers a method to boot up over network without the need for a hard drive or boot diskette. Configuration options: [Do not launch] [UEFI only] [Legacy only].

Wake On LAN

[Disabled]

This allows enabling or disabling power up the EPC when the LAN controller receives a call while the EPC is in Soft-Off or Hibernate mode. Configuration options: [Disabled] [Enabled].

Wake On Ring

[Disabled]

This allows enabling or disabling power up the EPC when the modem receives a call while the EPC is in Soft-Off or Hibernate mode.



The EPC cannot receive or transmit data until the system and applications are fully running, thus connection cannot be made on the first try. Turning an external modem off and then back on while the EPC is off causes an initialization string that will cause the system to power on.

Configuration options: [Disabled] [Enabled].

Wake On Time

[Disabled]

This allows enabling or disabling power up the EPC on specific time while the system is in Soft-Off or Hibernate mode. If this option is enabled a wake time is programmable within next two lines. The first line is setting the hour in international format (0-23). The second line is setting the minutes (0-59).

Configuration options: [Disabled] [Enabled].



Wake time is also adjustable from OS and can override this option.

Wake On USB/PS2

[Enabled]

Enable or Disable wakeup system via USB / PS2 while the system is in S3. If set to [Auto] it will follow OS related settings. PS2 can only wake up when set to [Enabled]. Setting [Auto] and [Disabled] are duplicated to Wake On LAN S3.

Configuration options: [Disabled] [Enabled].

Wake On PCIe Slot

[Disabled]

This allows enabling or disabling power up the EPC when a device mounted in a PCIe slot receives a call while the EPC is in Soft-Off or Hibernate mode. Configuration options: [Disabled] [Enabled].

Wake On LAN (S3)

Enable or Disable wakeup system via LAN while the system is in S3. If set to [Auto] it will follow OS related settings.

Configuration options: [Disabled] [Enabled].

Wake-on Modes

Please note that you have to shut down the system in power saving modes by OS before you can use Wake-on modes. Switching off the system by main power switch or front button-override will not initialize system

[Disabled]

wakeup functions. See following table, which wakeup events are available from different power states:

	Standby (S3)	Hibernate (S4)	Soft off (S5)
Front Button	Yes	Yes	Yes
LAN	Yes (Note5)	Yes (Note1)	Yes (Note1)
Modem (Note2)	Yes	Yes	Yes
USB/PS2 (Note3)	Yes	No	No
PCIe Slot (Note4)	Yes	Yes	Yes

Note 1: "Yes" is valid only, if the option <Wake on LAN> is [Enabled]. Note 2: "Yes" is valid only, if the option <Wake on Ring> is [Enabled]. Note 3: "Yes" is valid only, if the option <Wake on USB/PS2> is [Enabled]. Note 4: "Yes" is valid only, if the option <Wake on PCIe Slot> is [Enabled]. Note 5: "Yes" is valid only, if the option <Wake on LAN S3> is [Enabled].

Restore AC Power Loss

Select the options to reboot the system after power has been interrupted. [Power off] leaves the system off until pressing the power button. [Last State] reboots the system automatically if it was active before power loss. [Follow AC Power] will start up anytime power is available.

Configuration options: [Power off] [Last State] [Follow AC Power].

Power Button

If this option is [Disabled] the power button is locked when system is running. If this option is set to [Auto] the power button is locked only if the option Restore AC Power Lost is set to [Follow AC Power] Configuration options: [Disabled] [Enabled] [Auto].

Bootorder Menu

[Enabled]

[Enabled]

This option enables or disables the popup menu to change the boot order by pressing F10 during POST.

Configuration options: [Disabled] [Enabled].

[Follow AC Power]

Sub Menu ➤ CPU Configuration

Items marked with '*' are not available by all Intel processors

CPU Configuration	
Intel (R) Core (TM) i3-4330TE CPU 2.40GHz	
Hyper Threading	[Enabled]
Active Processor Core	[AII]
Overcklocking lock	[Disabled]
Limit CPUID Maximum	[Disabled]
Execute Disable Bit	[Enabled]
Intel Virtualization Technology	[Enabled]
Hardware Prefetcher	[Enabled]
Adjacent Cache Line Prefech	[Enabled]
CPU AES	[Enabled]
Boot performance mode	[Max Non-Turbo Per]
EIST	[Enabled]
Turbo Mode*	[Enabled]
Intel TXT(LT) Support	[Disabled]

CPU Configuration Parameters

Hyper-threading

[Enabled]

[AII]

This option allows enabling or disabling the Intel(R) Hyper-threading Technology to uses processor resources more efficiently, enabling multiple threads to run on each core.

Configuration options: [Disabled] [Enabled].

Active Processor Cores

This option enables all or several cores of an Intel processor. The numbers of cores are depends of used processor type. Configuration options: [All] [Number of cores 1-x].

Overclocking lock

This option enables or disables the overclocking feature of an Intel processor.

[Disabled]

Configuration options: [Disabled] [Enabled].

Limit CPUID Maximum

When CPUID instruction is executed, newer CPU may return a value greater than 3 which cause a certain problem with specific operating systems. Enabling "Limit CPUID Maximum" will limit the returned value to 3 (and less) to get rid of the problem. The problem does not occur with Windows series operating systems.

Configuration options: [Disabled] [Enabled]

Execute Disable Bit

Execute Disable Bit (EDB) is an Intel[©] Hardware-based security feature that can help reduce system exposure to viruses and malicious code. EDB allows the processor to classify areas in memory where application code can or cannot execute.

Configuration options: [Disabled] [Enabled]

Intel Virtualization Technology

Virtualization enhanced by Intel[©] Virtualization Technology will allow a system to run multiple operating systems and applications in independent partitions. With virtualization, one computer system can function as multiple "virtual" systems.

Configuration options: [Disabled] [Enabled]

Hardware Prefetcher

The processor has a hardware prefetcher that automatically analyses its requirements and prefetches data and instructions from the memory into the Level 2 cache. This reduces the latency associated with memory reads. Configuration options: [Disabled] [Enabled]

Adjacent Cache Line Prefetch

The processor has a hardware adjacent cache line prefetch mechanism that automatically fetches an extra 64-byte cache line whenever the processor requests for a 64-byte cache line. This reduces cache latency by making the next cache line immediately available if the processor requires it as well.

Configuration options: [Disabled] [Enabled]

CPU AES *

[Enabled]

[Enabled]

[Enabled]

[Disabled]

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[Enabled]

This option enables or disables the CPU Advanced Encryption Standard instructions.

Configuration options: [Disabled] [Enabled]

Boot performance mode

This option adjusts the performance of Boot mode. Configuration options: [Max Non-Turbo Performance] [Max Battery] [Turbo Performance]

EIST

[Enabled]

[Max Non-Turbo Performance]

This option enables or disables the Enhanced Intel SpeedStep Technology of an Intel processor.

Configuration options: [Disabled] [Enabled].

Turbo Mode *

This option enables or disables the Turbo Mode of several Intel processors. Configuration options: [Disabled] [Enabled]

Intel TXT (LT) Support *

Intel Trusted Execution Technology (TXT) is supported by several Intel processors.

Configuration options: [Disabled] [Enabled]

[Disabled]

[Enabled]

Sub Menu > SATA Configuration

SATA Controller(s) SATA Mode selection	[Enabled] [IDE]
SATA 0 (white)	ST500xxx
SATA 1 (blue)	Empty
SATA 4 (mSATA)	Empty
SATA 5 (black)	Empty

SATA Controller(s)

This option enables or disables the SATA controller. Configuration options: [Disabled] [Enabled].

SATA Mode selection

This option selects the SATA controller mode. The option [RAID] is only available on L1 boards with Q87 Chipset. Configuration options: [AHCI] [IDE][RAID].

Following two menu items are only available if SATA Mode selection is set to AHCI:

Aggressive LPM Support

This option enables or disables the PCH to aggressively enter link power state. This is only supported under AHCI mode. Configuration options: [Disabled] [Enabled].

SATA Controller Speed

This option indicates the maximum speed the SATA Controller can support.

[Enabled]

[Default]

[Enabled]

[IDE]

SATA 0, 1, 5

If option SATA Controller(s) is enabled SATA ports 0, 1 and 5 can handle hard disks and CD-, DVD- or Blu-ray devices. For each port are following options available if AHCI mode is enabled:

Hot Plug	[Disabled] [Enabled]
Spin Up Device	[Disabled] [Enabled]

SATA 4

SATA port 4 is a mSATA port which cannot handle hard disks and CD-, DVD- or Blu-ray devices.

L1 and L2 Motherboard, User Manual

Sub Menu > PCH-FW Configuration

ME FW Version ME Firmware Mode ME Firmware Type MF Firmware SKU NFC Capability State MEBx Type MDES BIOS State Code ► Firmware Update Configuration 9.1.1.1000 Normal Mode Full Sku Firmware 1.5MB Disabled [None] [Disabled]

Intel ME (Management Engine) Subsystem

Intel ME (Management Engine) Subsystem is a separated microcontroller embedded in the chipset driven by a special firmware. For systems with mainboard "L1.0-Q87-uATX-STD" the ME handles an own setup and can be entered while booting the system by pressing the keys CTRL + P.

MEBx Type

This option is only available on L2 boards with H81 chipset and selects the

type of MEBx.

MDES BIOS State Code

This option enables or disables the MDES BIOS State Code. Configuration options: [Disabled] [Enabled]

Sub Menu → PCH-FW Configuration → Firmware Update Configuration

Configuration options: [None] [miniMEBx]

ME FW Image Re-Flash

This option enables or disables the ME FW Image Re-Flash. Configuration options: [Disabled] [Enabled]

[None]

[Disabled]

[Disabled]

Sub Menu > AMT Configuration

Intel[®] Active-Management-Technology is a technology for remotely managing and securing PCs out-of-band. Intel AMT is only applicable with L1.0-Q87-uATX-STD mainboards.

Intel AMT	[Enabled]
BIOS Hotkey Pressed	[Disabled]
MEBx Selection Screen	[Disabled]
Hide Un-Configure ME Confirmation	[Disabled]
MEBx Debug Message Output	[Disabled]
Un-Configure ME	[Disabled]
AMT Wait Timer	0
Disable ME	[Disabled]
ASF	[Enabled]
Activate Remote Assistance Process	[Disabled]
USB Configure	[Disabled]
PET Progress	[Enabled]
AMT CIRA Timeout	0
WatchDog	[Disabled]
OS Timer	0
BIOS Timer	0

Intel AMT

[Enabled]

Intel Active Management Technology (AMT) is a special firmware to maintenance the system with remote systems. This option enables or disables the AMT. All other options are not available if AMT is disabled. Configuration options: [Disabled] [Enabled]

BIOS Hotkey Pressed

[Disabled]

This option enables or disables the BIOS hotkey press. Configuration options: [Disabled] [Enabled]

MEBx Selection Screen

This option enables or disables the MEBx selection screen. Configuration options: [Disabled] [Enabled]

Hide Un-Configure ME Confirmation

This option hides Un-Configure ME without password confirmation prompt. Configuration options: [Disabled] [Enabled]

MEBx Debug Message Output

This option enables or disables the MEBx Debug message output. Configuration options: [Disabled] [Enabled]

Un-Configure ME

This option Un-Configures ME without password. Configuration options: [Disabled] [Enabled]

AMT Wait Timer

This option sets the timer to wait before sending Configuration options: [Number of seconds]

Disable ME

[Disabled]

This option sets the ME to soft temporary disabled. Configuration options: [Disabled] [Enabled]

ASF

[Enabled]

This option enables or disables the Alert Specification Format. Configuration options: [Disabled] [Enabled]

Activate Remote Assistance Process

This option triggers the CIRA boot. Configuration options: [Disabled] [Enabled]

USB Configure

This option enables or disables the USB configuration function. Configuration options: [Disabled] [Enabled]

PET Progress

This option enables or disables the PET events progress. Configuration options: [Disabled] [Enabled]

[Disabled]

[Disabled]

[Disabled]

[Disabled]

[0]

[Disabled]

[Enabled]

[Enabled]

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WatchDog

[Disabled]

This option enables or disables the Watchdog timer. Configuration options: [Disabled] [Enabled]

Sub Menu ➤ USB Configuration

USB Configuration	
USB Modul Version USB Controllers: 2 EHCIs, 1 XHCI USB Devices: 1 Drive, 1 Keyboard	8.10.36
Legacy USB Support XHCI Hand-off EHCI Hand-off XHCI Mode XHCI Pre-Boot Driver Route USB 2.0 pins to which HC? Enable USB 3.0 pins USB 3.0 PIN #1 USB 3.0 PIN #2 USB Ports Per-Port Disable Control USB Mass Storage Driver Support PORT 60/64 Emulation Device power-up delay	[Enabled] [Enabled] [Enabled] [Manual] [Enabled] [Route all Pins to EHCI] [Select Per-Pin] [Enabled] [Enabled] [Disabled] [Enabled] [Enabled] [Auto]

Legacy USB Support

[Enabled]

This option enables the legacy USB support. AUTO option disables legacy support if no USB devices are connected. Disable this option keep USB devices available only for EFI applications. Configuration options: [Disabled] [Enabled] [Auto]

XHCI Hand-off

[Enabled]

Enables support for operating systems without an XHCI hand-off feature. Configuration options: [Disabled] [Enabled]

EHCI Hand-off

Enables support for operating systems without an EHCI hand-off feature. Configuration options: [Disabled] [Enabled]

XHCI Mode

Mode of Operation of XHCI controller.

Configuration options: [Smart Auto] [Auto] [Enabled] [Disabled] [Manual]

XHCI Pre-Boot Driver

Enables or disable XHCI Pre-Boot Driver options for USB3.0 mode. Configuration options: [Enabled] [Disabled]

Route USB 2.0 pins to which HC?

This option route the USB 2.0 Pins to EHCI- or XHCI Controller. Configuration options: [Route per-Pin] [Route all Pins to EHCI] [Route all Pins to XHCI

Enable USB 3.0 pins

Enables or disable XHCI SuperSpeep support for each USB3.0 Port. Configuration options: [Select Per-Pin] [Disable all Pins] [Enable all Pins]

USB Ports Per-Port Disable Control

This option control USB ports disabling. If option is enabled each USB port can be separately disabled.

Configuration options: [Disabled] [Enabled]

USB Mass Storage Driver Support

This option enables or disables the USB Mass storage driver support. Configuration options: [Disabled] [Enabled]

PORT 60/64 Emulation

This option enables or disables the I/O port 60/64h emulation support. Configuration options: [Disabled] [Enabled]

Device Power-up delay

Maximum time the device will take before it properly reports itself to the host controller. [Auto] uses default values: for a root port it is 100ms, for a Hub port the delay is taken from the Hub descriptor. Configuration options: [Auto] [Manual]

[Select Per-Pin]

[Route all Pins to EHCI]

[Enabled]

[Auto]

[Enabled]

[Enabled]

[Disabled]

[Manual]

[Enabled]

Device Power-up delay in seconds	[5]
----------------------------------	-----

This time delay is for each USB-Storage Device.

Sub Menu > NTC6106D Super IO Configuration

NTC6106D Super IO Configuration	
NTC6106D Super IO Chip	NTC6106D
 Serial Port 1 Configuration Serial Port 2 Configuration 	
Parallel Port Configuration	

The Super IO Configuration for Serial Port 1 and 2 are only an information sub menu which shown their associated properties.

Sub Menu ≻ NTC6106D Super IO Configuration ≻ Parallel Port Configuration

Parallel Port [Enabled]

This option enables or disables parallel port (LPT/LPTE). Configuration options: [Disabled] [Enabled]

Changing Settings [Auto]

This option selects several settings for parallel port (LPT/LPTE). Configuration options: [Auto] [IO=378h; IRQ=5] [IO=378h; IRQ=5,6,7,10,11,12] [IO=278h; IRQ=5,6,7,10,11,12] [IO=3BCh; IRQ=5,6,7,10,11,12]

Device Mode [ECP Mode]

This option selects several options of the printer port mode (LPT/LPTE). Configuration options: [STD Printer Mode] [SPP Mode] [EPP-1.9 and SPP Mode] [EPP-1.7 and SPP Mode] [ECP Mode] [ECP and EPP-1.9 Mode] [ECP and EPP-1.7 Mode]

Sub Menu ➤ NTC6106D H/W Monitor

PC Health Status	
SYS Thermistor Temp PECI Agento Temp CPU Diode Temp AUXTIN Tem CPU Fan Speed PSU Fan Speed	: +30°C / +86°F : +89.5°C / +193°F : +88.5°C / +191°F : +45°C / +113°F : 1630 RPM : 1285 RPM
VCORE	: +1.056 V
+5V (VIN0)	: +5.032 V
+12V (VIN1)	: +12.406 V
AVCC	: +3.396 V
VCC3V	: +3.331 V
VSB3	: +3.356 V
VBAT	: +3.024 V

The H/W Monitor is only an information screen which shows all system temperatures, voltages and fan speeds.

Sub Menu > Serial Port Console Redirection

COM0 (Disabled) Console Redirection Port is Disabled COM1 (PCI Bus0, Dev0, Func0) Port is Disabled Legacy Console Redirection > Legacy Console Redirection Settings Serial Port for Out-of-Band Management/ Windows Emergency Management Services (EMS) Console Redirection Settings

Sub Menu ➤ Serial Port Console Redirection ➤ Legacy Console Redirection Settings

Legacy Console Redirection Port [COM0 (Disabled)] This option select a COM Port to display redirection of legacy OS and

legacy Option ROM messages.

Console Redirection

[Enabled]

This option enables or disables the console redirection. Configuration options: [Disabled] [Enabled]

Sub Menu ➤ Serial Port Console Redirection ➤ Console Redirection Settings

Out-of-Band Mgmt	[Com0 (Disabled)]
Terminal Type	[VT-UTF8]
Bits per second	[115200]
Flow Control	[None]
Data Bits	8
Parity	None
Stop Bits	1

Out-of-Band Mgmt

[Com0 (Disabled)]

This option enables or disables COM0 or COM1 for serial redirection. Configuration options: [Com0 (Disabled)] [COM1(PCI Bus0,Dev0,Func0) (Disabled)]

Terminal Type

This option selects a preferred terminal type for Out-of-Band Management.

Configuration options: [VT100] [VT100+I] [VT-UTF8] [ANSI]

Bits per second

[115200]

[VT-UTF8]

This option selects the serial port transmission speed. Configuration options: [9600] [19200] [57600] [115200]

Flow Control

[None]

This option selects the serial port flow control.

Flow Control can prevent data loss from buffer overflow, when sending data. If the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty a 'start' signal can be sent to restart the flow. Hardware flow control uses two wires to send start/stop signals.

Configuration options: [None] [Hardware RTS/CTS] [Software Xon/Xoff]

Sub Menu > AMI Graphic Output Protocol Policy

This sub menu is only available if the CSM module is disabled or the graphic output of the CSM module is redirected from legacy VBIOS (Video BIOS) to UEFI GOP (Graphics Output Protocol)

Intel (R) Haswell Graphic Controller Intel (R) GOP Driver [5.0.1036]

Output Select BIST Enable [DVI] [Disabled]

Output Select

[DVI1]

This option enable the graphic output device while POST is running Configuration options: [DVI1] [CRT1] (Depends of connected displays)

BIST Enable

[Disabled]

This option enable the **B**uilt In **S**elf **T**est of the connected displays. Configuration options: [Disabled] [Enabled]

Sub Menu > Network Stack Configuration

Network Stack	[Enabled]
Ipv4 PXE Support	[Enabled]
PXE boot wait time	[0]
Media detect count	[1]

Network Stack

This option enable the UEFI Network stack and release the options: Ipv4 PXE Support, PXE boot wait time and Media detect count. Configuration options: [Disabled] [Enabled]

IPv4 PXE Support

This option enable the IPv4 PXE boot support Configuration options: [Disabled] [Enabled]

PXE boot wait time

This option set the wait time to press ESC key to abort the PXE boot Configuration options: [Number of seconds]

Media count detect

This option determines how often the presence of media is checked. Configuration options: [Number of times]

[Disabled]

[Enabled]

[1]

[0]

Sub Menu > CMOS

CMOS Settings/Information		
Bad battery detected:	[False]	
First boot detected	[False]	
Defaults loaded	[False]	
Bad checksum detected	[False]	

The CMOS menu is only an information screen, which shows some specific CMOS information.

Sub Menu > Intel(R) Ethernet Network Connection

Port Configuration Menu ➤ NIC Configuration	
Blink LED (range 0-15 seconds)	0
Port Configuration Information UEFI Driver: Adapter PBA: Device Name Chip Type: PCI Device ID: PCI Bus:Device:Function: Link Status Factory MAC Address:	Intel(R) 1GbE DEV 5.1.00 FFFFFF-0FF Intel ® Ethernet Connec Intel PCH LPT 153A 0:25:0 [Disconnected] AC:DC:FA:DE:BE:EF
Factory MAC Address:	AC:DC:FA:DE:BE:EF

Sub Menu ➤ Intel(R) Ethernet Network Connection ➤ NIC Configuration

Link Speed

[AutoNeg]

This option selects a preferred link speed and duplex for current LAN port. Configuration options: [AutoNeg] [10 Mbps Half] [10 Mbps Full] [100 Mbps Half] [100 Mbps Full].

Wake On LAN

[Enabled]

This allows enabling or disabling power up the EPC when the LAN controller receives a call while the EPC is in Soft-Off or Hibernate mode. Configuration options: [Disabled] [Enabled].

Chipset Menu

PCH-IO Configuration

System Agent (SA) Configuration

Sub Menu > PCH-IO Configuration

BIOS Security Configuration

Sub Menu ➤ PCH-IO Configuration ➤ BIOS Security Configuration

BIOS Security Configuration

BIOS Lock BIOS Interface Lock [Enabled] [Enabled]

BIOS Lock

[Enabled]

This option enables or disables the BIOS lock enable (BLE) bit. Configuration options: [Disabled] [Enabled]

BIOS Interface Lock [Enabled] This option handles the NVRAM access. For using a NVRAM

This option handles the NVRAM access. For using a NVRAM module set this option to [Disabled]. Configuration options: [Disabled] [Enabled]

Haswell

1.7.0.0

Supported

Sub Menu > System Agent (SA) Configuration

System Agent Bridge Name System Agent RC Version Vt-d Capability

Graphics Configuration

Sub Menu ➤ System Agent (SA) Configuration ➤ Graphics Configuration

Graphics Configuration	
IGFX VBIOS Version	1027
IGFX Frequency	700 MHz
Graphics Turbo IMON Current	31
Primary Display	[Auto]
Primary PEG	[Auto]
Primary PCIE	[Auto]
Internal Graphics	[Auto]
Aperture Size	[256MB]
DVMT Pre-Allocated	[32MB]
DVMT Total Gfx Mem	[256MB]
Gfx Low Power Mode	[Enabled]
Internal Graphics Port Order	[Disp.1>VGA]

Graphics Turbo IMON Current

This option sets the Graphics Turbo IMON current value. Configuration options: 14-31

Primary Display

This option selects which graphics device IGFX, PEG, PCI should be the primary display. Configuration options: [Auto] [IGFX] [PEG] [PCIE]

Primary PEG

This option selects which graphics device PEG11, PEG12 should be the primary PEG. Configuration options: [Auto] [PEG11] [PEG12]

Primary PCIE

This option selects which graphics device PCIE1-7 should be the primary PCI. Configuration options: [Auto] [PCIE1] [PCIE2] [PCIE3] [PCIE4] [PCIE5] [PCIE6] [PCIE7]

Internal Graphics

This option keeps the IGD enabled based on the setup option. Configuration options: [Auto] [Disabled] [Enabled]

Aperture Size

This option sets the aperture size of the graphics device. Configuration options: [128MB] [256MB] [512MB]

DVMT Pre-Allocated

This option selects DVMT 5.0 pre-allocated graphics memory size used by the internal graphics device. Configuration options: [32MB] - [1024MB]

DVMT Total Gfx Mem

This option selects DVMT 5.0 total graphics memory size used by the internal graphics device. Configuration options: [128MB] [256MB] [MAX]

Gfx Low Power Mode

[32MB]

[256MB]

[256MB]

[Enabled]

[Enabled]

[Auto]

[Auto]

[Auto]

[Auto]

This option is application for SFF only Configuration options: [Disabled] [Enabled]

Internal Graphics Port Order

[Disp.1>VGA]

This option selects which device may be used as primary display first Configuration options: [VGA>Disp.1] [Disp.1> VGA]

Boot Menu

The Boot Menu enables you to set the order of bootable devices to a regular base. Pressing the function key <F10> while POST is running will change the boot order only once. You will see a Pop-Up window listing all devices the system is able to boot from. Select the boot device with keys <Up> and <Down>. Press <Enter> key to start the selected device booting.

Please select boot device:	
IBA GE Slot 00C8 v1365	
Skip Selection	
	↑ and ↓ to
move selection	
ENTER to select boot device	
ESC to boot using defaults	

•	Select the keyboard Numlock State. Configuration options: [On] [Off]	•
Quiet I	Boot	[
	This option disables the POST messages while booting Configuration options: [Disabled] [Enabled]	; .
Fast Bo	oot	[
	Enables or disables boot with initialization of minimal	S

F al set of devices required to launch activate boot option. Has no effect for BBS boot options Configuration options: [Disabled] [Enabled]

5

[On]

[Disabled]

[Disabled]

[LEGACY]

[Hard Disk]

[Disabled]

[Disabled]

[Disabled]

[Disabled]

[Disabled]

[USB CD/DVD]

[Disabled]

[Disabled]

[On]

Boot Configuration Setup Prompt Timeout

Quiet Boot

Boot mode select

Boot Option #1

Boot Option #2

Boot Option #3

Boot Option #4

Boot Option #5

Boot Option #6

Boot Option #7

CSM parameters

Fast Boot

Bootup NumLock State

Fixed Boot Order Priorities

Bootup NumLock State

S

➤Hard Disk Drive BBS Priorities Network Drive BBS Priorities USB Key Drive BBS Priorities

Quiet Boo

Boot mode select

[LEGACY]

This option selects the boot mode.

If UEFI is choose only boot from UEFI devices. All other devices will not shown

Configuration options: [LEGACY] [UEFI]

Fixed Boot Order Priorities '#n' Boot Device

These menu entries are used to specify the boot sequence from the available devices. Every entry (from #1 till #7) specifies a boot group. Each boot device found while POST is running will be sorted in one of a BBS group. Enter a BBS group to swap the boot order of the BBS device.

Sub Menu ➤ CSM Parameters

This option is only available if the CSM module was not disabled by activating the Secure Boot option.

Launch Storage OpROM policy Launch Video OpROM policy	[Legacy only] [Legacy only]	
Other PCI device ROM priority	[Legacy OpROM]	

Launch Storage OpROM policy

This option controls the execution of UEFI or Legacy Option ROMs. Configuration options: [Do not launch] [UEFI only] [Legacy only]

Launch Storage OpROM policy

This option controls the execution of UEFI or Legacy Video ROMs. Configuration options: [Do not launch] [UEFI only] [Legacy only]

Launch Storage OpROM policy

This option controls the execution of OpROMs of PCI devices other than network, mass storage or video devices.

Configuration options: [Legacy OpROM] [UEFI OpROM]

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[Legacy only]

[Legacy only]

[Legacy OpROM]

Security Menu

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Password Description	
If only the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup. The password length must be In the following range Minimum length	3
Maximum length	20
Administrator Password Intrusion detection Case Status	[Disabled] [Closed]
Secure Boot menu	
HDD Security Configuration: Hard Disk #1	

Administrator Password

This field allows you to set the password. Highlight the field and press <Enter>. Type a password and press <Enter>, you can type 3 to 20 alphanumeric characters. Symbols and other characters are ignored. To confirm the password, type the password again and press <Enter>. This password allows full access to the UEFI BIOS Setup menu. To clear the password, highlight this field and press <Enter>. Enter your current password. Then you will be asked to enter the new password. Press <Enter> and the password will be deleted.

Intrusion Detection

[Disabled]

If the system cover is removed and the Intrusion Detection is [Enabled], the system stops during the next reboot or power up process and display a warning message. After this warning the boot process stops and the user has to enter the UEFI BIOS setup which resets the open case detection automatically. Additionally is a viewing point of the case open switch below the enable/disable entry point placed. This message will signalize the actual case open status directly. Configuration Options: [Disabled] [Enabled]

Sub Menu ➤ Secure Boot menu

System Mode	Setup
Secure Boot	Not Active
Secure Boot	[Enabled]
Secure Boot Mode	[Standard]

Secure Boot

[Enabled]

This option can be enabled if:

- 1. System running in User mode with enrolled Platform Key
- 2. CSM function is disabled

Configuration options: [Disabled] [Enabled]

Secure Boot Mode

[Standard]

This is the secure mode selector. The 'Custom' mode enables user to change the Image Execution policy and mange Secure Boot Keys Configuration options: [Standard] [Custom]

Save & Exit Menu

Save Changes and Reset Discard Changes and Exit Discard Changes and Reset

Save Options Save Changes Discard Changes

Restore Defaults

Boot Override USB Drive Hard Drive Network Card

Launch EFI Shell from filesystem device

Once made all selections from the various menus in the Setup program, save changes should execute and leave Setup. Select Exit from the menu bar to display the following menu.

Save Changes and Reset

Once finished changing setup values, this option from the Exit menu ensure that values are saved to the NVRAM. The NVRAM is sustained by an onboard backup battery and stays on even when the EPC is turned off. Once this option is selected, a confirmation is asked. Select [Ok] to save changes and reset the system.

Discard Changes and Exit

This option should only be used if the changes made in Setup should not save. If made some changes to fields other than system date, system time, and password, the system will ask for confirmation before exiting.

Discard Changes and Reset

This option should only be used if the changes made in Setup should not save. If made some changes to fields other than system date, system time, and password, the system will ask for confirmation before exiting and reset the system.

Restore Defaults

This option loads the default values for each of the values on the Setup menu. When this option is selected or if <F3> is pressed, a confirmation is requested. Select [Ok] to load default values. Now select Exit Saving to save the default values or make other changes before saving the values to the non-volatile RAM.

Boot Override

With this option choose a boot device that is listed below this menu entry. Every entry specifies a boot device that enumerate during POST.

Launch EFI Shell from File System Device

Attempts to launch EFI shell application from one of the available file system devices.

Test Points Codes

At the beginning of each POST routine, the UEFI BIOS outputs the test point error code to I/O port address 80h. Use this code during trouble shooting to establish where the system failed and what routine has been performed.

Checkpoint Ranges

Status Code Range	Description
0x01 – 0x0B	SEC execution
0x0C – 0x0F	SEC errors
0x10-0x2F	PEI execution up to and including memory detection
0x30 – 0x4F	PEI execution after memory detection
0x50 – 0x5F	PEI errors
0x60 – 0x8F	DXE execution up to BDS
0x90 – 0xCF	BDS execution

0xD0 – 0xDF	DXE errors
0xE0 – 0xE8	S3 Resume (PEI)
0xE9 – 0xEF	S3 Resume errors (PEI)
0xF0 – 0xF8	Recovery (PEI)
0xF9 – 0xFF	Recovery errors (PEI)

SEC Phase

Status Code	Description
0x00	Not used
Progress Codes	
0x01	Power on. Reset type detection (soft/hard).
0x02	AP initialization before microcode loading
0x03	North Bridge initialization before microcode loading
0x04	South Bridge initialization before microcode loading
0x05	OEM initialization before microcode loading
0x06	Microcode loading
0x07	AP initialization after microcode loading
0x08	North Bridge initialization after microcode loading
0x09	South Bridge initialization after microcode loading
0x0A	OEM initialization after microcode loading
0x0B	Cache initialization
SEC Error Codes	
0x0C – 0x0D	Reserved for future AMI SEC error codes
0x0E	Microcode not found
0x0F	Microcode not loaded

PEI Phase

Status Code	Description
Progress Codes	
0x10	PEI Core is started
0x11	Pre-memory CPU initialization is started
0x12	Pre-memory CPU initialization (CPU module specific)
0x13	Pre-memory CPU initialization (CPU module specific)

0x14	Pre-memory CPU initialization (CPU module specific)
0x15	Pre-memory North Bridge initialization is started
0x16	Pre-Memory North Bridge initialization (North Bridge module specific)
0x17	Pre-Memory North Bridge initialization (North Bridge module specific)
0x18	Pre-Memory North Bridge initialization (North Bridge module specific)
0x19	Pre-memory South Bridge initialization is started
0x1A	Pre-memory South Bridge initialization (South Bridge module specific)
Ox1B	Pre-memory South Bridge initialization (South Bridge module specific)
0x1C	Pre-memory South Bridge initialization (South Bridge module specific)
0x1D - 0x2A	OEM pre-memory initialization codes
0x2B	Memory initialization. Serial Presence Detect (SPD) data reading
0x2C	Memory initialization. Memory presence detection
0x2D	Memory initialization. Programming memory timing information
0x2E	Memory initialization. Configuring memory

0x2F	Memory initialization (other).
0x30	Reserved for ASL (see ASL Status Codes section below)
0x31	Memory Installed
0x32	CPU post-memory initialization is started
0x33	CPU post-memory initialization. Cache initialization
0x34	CPU post-memory initialization. Application Processor(s) (AP) initialization
0x35	CPU post-memory initialization. Boot Strap Processor (BSP) selection
0x36	CPU post-memory initialization. System Management Mode (SMM) initialization
0x37	Post-Memory North Bridge initialization is started
0x38	Post-Memory North Bridge initialization (North Bridge module specific)
0x39	Post-Memory North Bridge initialization (North Bridge module specific)

0x3A	Post-Memory North Bridge initialization (North Bridge module specific)	
0x3B	Post-Memory South Bridge initialization is started	
0x3C	Post-Memory South Bridge initialization (South Bridge module specific)	
0x3D	Post-Memory South Bridge initialization (South Bridge module specific)	
0x3E	Post-Memory South Bridge initialization (South Bridge module specific)	
0x3F-0x4E	OEM post memory initialization codes	
0x4F	DXE IPL is started	
PEI Error Codes		
0x50	Memory initialization error. Invalid memory type or incompatible memory speed	
0x51	Memory initialization error. SPD reading has failed	

0x52	Memory initialization error. Invalid memory size or
	memory modules do not match.
0x53	Memory initialization error. No usable memory detected
0x54	Unspecified memory initialization error.
0x55	Memory not installed
0x56	Invalid CPU type or Speed
0x57	CPU mismatch
0x58	CPU self test failed or possible CPU cache error
0x59	CPU micro-code is not found or micro-code update is failed
0x5A	Internal CPU error
0x5B	reset PPI is not available
0x5C-0x5F	Reserved for future AMI error codes
S3 Resume Progress Codes	
0xE0	S3 Resume is stared (S3 Resume PPI is called by the DXE IPL)
0xE1	S3 Boot Script execution
0xE2	Video repost
0xE3	OS S3 wake vector call
0xE4-0xE7	Reserved for future AMI progress codes
S3 Resume Er	ror Codes
0xE8	S3 Resume Failed
0xE9	S3 Resume PPI not Found
OxEA	S3 Resume Boot Script Error
OxEB	S3 OS Wake Error
0xEC-0xEF	Reserved for future AMI error codes
Recovery Progress Codes	
0xF0	Recovery condition triggered by firmware (Auto recovery)
0xF1	Recovery condition triggered by user (Forced recovery)
0xF2	Recovery process started

0xF3	Recovery firmware image is found
0xF4	Recovery firmware image is loaded
0xF5-0xF7	Reserved for future AMI progress codes
Recovery Error Codes	
0xF8	Recovery PPI is not available
0xF9	Recovery capsule is not found
0xFA	Invalid recovery capsule
0xFB – 0xFF	Reserved for future AMI error codes

PEI Beep Codes

# of Beeps	Description
1	Memory not Installed
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

DXE Phase

Status Code	Description
0x60	DXE Core is started
0x61	NVRAM initialization
0x62	Installation of the South Bridge Runtime Services
0x63	CPU DXE initialization is started

0x64	CPU DXE initialization (CPU module specific)
0x65	CPU DXE initialization (CPU module specific)
0x66	CPU DXE initialization (CPU module specific)
0x67	CPU DXE initialization (CPU module specific)
0x68	PCI host bridge initialization
0x69	North Bridge DXE initialization is started
0x6A	North Bridge DXE SMM initialization is started
0x6B	North Bridge DXE initialization (North Bridge module specific)
0x6C	North Bridge DXE initialization (North Bridge module specific)
0x6D	North Bridge DXE initialization (North Bridge module specific)
0x6E	North Bridge DXE initialization (North Bridge module specific)
0x6F	North Bridge DXE initialization (North Bridge module specific)
0x70	South Bridge DXE initialization is started
0x71	South Bridge DXE SMM initialization is started
0x72	South Bridge devices initialization
0x73	South Bridge DXE Initialization (South Bridge module specific)
0x74	South Bridge DXE Initialization (South Bridge module specific)
0x75	South Bridge DXE Initialization (South Bridge module specific)
0x76	South Bridge DXE Initialization (South Bridge module specific)
0x77	South Bridge DXE Initialization (South Bridge module specific)
0x78	ACPI module initialization
0x79	CSM initialization
0x7A –	Reserved for future AMI DXE codes
0x7F	
0x80 – 0x8F	OEM DXE initialization codes
0x90	Boot Device Selection (BDS) phase is started
0x91	Driver connecting is started
0x92	PCI Bus initialization is started
0x93	PCI Bus Hot Plug Controller Initialization
0x94	PCI Bus Enumeration
0x95	PCI Bus Request Resources
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0x96	PCI Bus Assign Resources
0x97	Console Output devices connect
0x98	Console input devices connect
0x99	Super IO Initialization
0x9A	USB initialization is started
0x9B	USB Reset

0x9C	USB Detect	
0x9D	USB Enable	
0x9E –	Reserved for future AMI codes	
0x9F		
0xA0	IDE initialization is started	
0xA1	IDE Reset	
0xA2	IDE Detect	
0xA3	IDE Enable	
0xA4	SCSI initialization is started	
0xA5	SCSI Reset	
0xA6	SCSI Detect	
0xA7	SCSI Enable	
0xA8	Setup Verifying Password	
0xA9	Start of Setup	
0xAA	Reserved for ASL (see ASL Status Codes section below)	
0xAB	Setup Input Wait	
0xAC	Reserved for ASL (see ASL Status Codes section below)	
0xAD	Ready To Boot event	
OxAE	Legacy Boot event	
0xAF	Exit Boot Services event	
0xB0	Runtime Set Virtual Address MAP Begin	
0xB1	Runtime Set Virtual Address MAP End	
0xB2	Legacy Option ROM Initialization	

0xB3	System Reset
0xB4	USB hot plug
0xB5	PCI bus hot plug
0xB6	Clean-up of NVRAM
0xB7	Configuration Reset (reset of NVRAM settings)
0xB8 – 0xBF	Reserved for future AMI codes
0xC0	OEM BDS initialization codes
0xCF	
DXE Error Codes	
0xD0	CPU initialization error
0xD1	North Bridge initialization error
0xD2	South Bridge initialization error
0xD3	Some of the Architectural Protocols are not available
0xD4	PCI resource allocation error. Out of Resources
0xD5	No Space for Legacy Option ROM
0xD6	No Console Output Devices are found
0xD7	No Console Input Devices are found
0xD8	Invalid password
0xD9	Error loading Boot Option (LoadImage returned error)
0xDA	Boot Option is failed (StartImage returned error)
0xDB	Flash update is failed
0xDC	Reset protocol is not available

DXE Beep Codes

# of Beeps	Description	
1	Invalid password	
4	Some of the Architectural Protocols are not available	
5	No Console Output Devices are found	
5	No Console Input Devices are found	
6	Flash update is failed	
7	Reset protocol is not available	
8	Platform PCI resource requirements cannot be met	

Abbreviations

ADM	AMI Display Manager
ACPI	Advanced Configuration and Power Interface
AGTL+	Assisted Gunning Transceiver Logic
APC	Advanced Power Control
APIC	Advanced Peripheral Interrupt Controller
APM	Advanced Power Management
AT	Advanced Technology
ΑΤΑ	AT Attachment
BAT	Basic Access Test
BBS	BIOS Boot Specification
BDA	BIOS Data Area
BGA	Ball Grid Array
BIOS	Basic Input and Output System
CMOS	Complementary Metal Oxide Semiconductor
CPLD	Complex Programmable Logic Device
CPU	Central Processing Unit
CRT	Cathode-ray Tube
DIM	Device Initialization Manager
DIMM	Dual Inline Memory Module
DMA	Direct Memory Access
DMI	Desktop Management Interface
DVMT	Dynamic Video Memory Technology
DVI	Digital Video Interface
EBDA	Extended BIOS Data Area
ECP	Extended Capabilities Port
EEPROM	Electrical Erasable Read Only Memory
EFI	Extensible Firmware Interface
E-IDE	Enhanced Integrated Drive Electronics
EMS	Expanded Memory System
ESCD	Extended System Configuration Data

EPP	Enhanced Parallel Port
FSB	Front Side Bus
GPNV	General Purpose Non-Volatile (RAM)
GTL	Gunning Transceiver Logic
HW	Hardware
IDE	Integrated Drive Electronics
IGD	Internal Graphic Device
IPL	Initial Program Load (Device)
LAN	Local Area Network
LBA	Logical Block Addressing
LCD	Liquid Crystal Display
MAC	Media Access Control
MTRR	Memory Type Range Register
MP	Multiple Processors
NA	Power failure
NVRAM	Non-volatile Random Access Memory
P -ATA	Parallel AT Attachment (old version of hard disk
	interface)
POS	Point of Sales
PCI	Peripheral Component Interconnect
PnP	Plug and Play
POST	Power On Self Test
RAM	Random Accessible Memory
RI	Ring Indicator
ROM	Read Only Memory
RS	Retail Systems
S ATA	Serial AT Attachment (new version of hard disk interface)

SLP	System Locked Pre-Installation
SMI	System Management Interrupt
SMM	System Management Mode
SMRAM	System Management RAM
SPGA	Staggered Pin Grid Array
TFT	Thin-film transistor
TPM	Trusted Platform Module
UEFI	Unified Extensible Firmware Interface
UPS	Uninterruptible Power Supply
USB	Universal Serial Bus
UUID	Universal Unique Identifier
VGA	Video Graphics Array
WOL	Wake On LAN
WOM	Wake On Modem

Diebold Nixdorf Systems GmbH D-33094 Paderborn

Order No.: 01750266922C